Electronic Patent Application Fee Transmittal							
Application Number:	10	10538216					
Filing Date:	09	09-Jun-2005					
Title of Invention:	VERTICAL INSULATED GATE TRANSISTOR AND MANUFACTURING METHOD						
First Named Inventor/Applicant Name:	Ju	Jurriaan Schmitz					
Filer:	Peter Zawilski						
Attorney Docket Number:	NL02 1416						
Filed as Large Entity							
U.S. National Stage under 35 USC 371 Filling Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Utility Appl Issue fee		1501	1	1400	1400		

Publ. Fee- early, voluntary, or normal

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Extension-of-Time:							
Miscellaneous:							
	Total in USD (\$)			1700			